

Errata - Introduction to Spintronics, Second Edition

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1. In Equations (13.66) and (13.77), the numerators and denominators in the right hand sides have been reversed.
2. Problem 13.4 should read:
Show that the maximum conductance on-to-off ratio of the spin relaxation transistor of Section 13.6.2 is

$$\frac{G_{on}}{G_{off}} = \frac{1}{1 - \zeta_s \zeta_D}$$

where ζ_s and ζ_D are the spin injection efficiency at the source/channel interface and spin detection efficiency at the drain channel interface, respectively. Note that you will not get this result by setting the spin relaxation length equal to infinity at zero gate voltage, and zero and non-zero gate voltage in Equation (13.66). Explain why.